ICCCC2018

Second Call for Papers (14.04.2017)

Previous edition: ICCCC 2016: http://univagora.ro/en/icccc2016
The Proceedings of ICCCC2016 has been published by IEEE
http://www.proceedings.com/30804.html
and indexed in IEEE Xplore and in ISI Web of Science (Thomson Reuters).
Some selected proceedings papers was extended and published in INTERNATIONAL JOURNAL OF COMPUTERS COMMUNICATIONS & CONTROL .

Scope and Topics

The International Conference on Computers Communications and Control (ICCCC) has been founded in 2006 by I. Dzitac, F.G. Filip and M.-J. Manolescu and organized every even year by Agora University of Oradea, under the aegis of the Information Science and Technology Section of Romanian Academy and IEEE - Romania Section.

2016 edition was Co-Sponsored by IEEE Region 8 - Europe, Middle East, Africa
The goal of this conference is to bring together international researchers, scientists in academia and industry to present and discuss in a friendly environment their latest research findings on a broad array of topics in computer networking and control.
The Program Committee is soliciting paper describing original, previously unpublished, completed research, not currently under review by another conference or journal, addressing state-of-the-art research and development in all areas related to computer networking and control.

ICCCC2018 General Program

http://www.baile-felix.ro/en/

DATE	HOURS	ACTIVITY	PLACE
Tuesday, ***May 08***	*12:00-22:00*	*Registration*	*Hotel President*
Wednesday **May 09**	07:00-08:30	Breakfast + Registration	Restaurant R1/Hotel President
	09:30-10:00	Official Opening Ceremony	Agora University
	10:00-11:00	Keynote lecture	Agora University
	11:00-11:30	Coffee break	Agora University
	11:30-12:30	Keynote lecture	Agora University
	12:30-13:30	Welcome cocktail	Agora University
	14:30-15:30	Lunch	Restaurant R1/Hotel President
	16:0 0-17:00	Plenary lecture	Hotel President/ Conference Room S1
	17:00-18:00	Plenary lecture	Hotel President/ Conference Room S1
	18:00-18:30	Coffee break	Hotel President/ Conference Room S1
	18:30-19:30	Parallel Sessions	Hotel President (S1, S2, S3, S4)
	20:00-23:00	Romanian Dinner	Restaurant R2/Hotel
Thursday **May 10**	07:00-08:30	Breakfast	Hotel President/Restaurant R1
	09:00-11:00	Parallel Sessions	Hotel President (S1, S2, S3, S4)
	11:00-11:30	Coffee break	Hotel President
	11:30-13:30	Parallel Sessions	Hotel President (S1, S2, S3, S4)
	13:30-14:00	Closing Ceremony	Hotel President S1
	14:30-15:30	Lunch	Hotel President/Restaurant R2
	15:30-19:30	Free activities	ORADEA CITY TOUR (with bus)
	20:00-23:00	International Dinner	Hotel President, Restaurant R2
Friday **May 11**	07:00-08:30 09:00-20:00	Breakfast EXCURSION	Hotel President/Restaurant R1 Mountain
Saturday **May 12**	07:00-11:00 09:00-12:00	Breakfast Departure of participants	

ICCCC2018 Chairs and Committees

General Chair:

Ioan DZITAC, Rector of Agora University of Oradea, Romania, rector@univagora.ro, professor.ioan.dzitac@ieee.org

Prof. Ioan Dzitac, PhD, Senior Member of IEEE (since 2011), is an information sciences professor at Aurel Vlaicu University of Arad -Romania (since 2009), Adjunct Professor at University of Chinese Academy of Sciences -Beijing, China (2013-2016) and Rector of Agora University of Oradea -Romania (2012-2020). He received B.Sc.(eq.M.Sc.) in Mathematics (1977) and Ph.D. in Information Sciences (2002) from Babes-Bolyai University of Cluj- Napoca, Romania. His current research interests include different aspects of Artificial Intelligence, Applications of Fuzzy Logic, Distributed Systems and Information Systems. He is co-founder and A. Editor-in-Chief of an ISI SCI Expanded quoted journal (2006), International Journal of Computers Communications & Control and member in Editorial Board of 7 scientific journals. Also he is co-founder and General Chair International Conference on Computers Communications and Control and he was member of the Program Committee of more than 70 international conferences and gave plenary papers and special sessions in universities and conferences held in China (4), Russia, Brazil, India, Lithuania and South Korea. He has published over 60 scientific papers, 2 books, 14 courses and materials for students, 4 conference proceedings and more than 60 scientific papers in journals and conferences proceedings.

Program Committee Chair:

Florin Gheorghe FILIP, President of "Information Science and Technology" Section of Romanian Academy, Romania, ffilip@acad.ro

Prof. Dr. Eng. Florin Gheorghe Filip is a full member od Romanian Academy, President of "Information Science and Technology" Section of Romanian Academy, and director of Library of Romanian Academy. He was a Vice-President of the Romanian Academy (2000 - 2010), Managing director of National Institute for R&D in Informatics (19911997). He authored/coauthored over 250 papers published in international journals (IFAC J Automatica, IFAC J Control Engineering Practice, Annual Reviews in Control, Computers in Industry, System Analysis Modeling

Simulation, Large Scale Systems etc) and contributed volumes printed by international publishing houses (Pergamon Press, Springer, Kluwer, Chapmann & Hall etc). He is also the author/coauthor of ten monographs. He was an IPC member of more than 50 international conferences held in Europe, USA, South America, Asia and Africa and gave plenary papers at scientific conferences held in Chile, China, France, Poland, Portugal, Rep. of Moldova, Spain, Tunisia, UK. He is co-founder and Editor-in-Chief of International Journal of Computers Communications & Control and co-founder of International Conference Computers Communications & Control**.** He has received Doctor Honoris Causa title from "Lucian Blaga" Univers ity of Sibiu (November 2000), "Valahia" University, Targoviste (2007), "Ovidius" University, Constanta (2007), Ecolle Centrale de Lille (France) (2007), Technical University ”Traian Vuia”, Timisoara (2009), Agora University of Oradea (2012) and Academy of Economic Studies, Bucharest (2014).

Organizing Committee Chair:

Misu-Jan MANOLESCU,
President of Agora University of Oradea, Romania, mmj@univagora.ro
Prof. Dr. Eng. Misu-Jan Manolescu is a Professor of Risk Management and President of Administration Council of Agora University. He is an Engineer, PhD in Energy Engineering, PhD in Management, president of Agora Foundation, Founder of Agora University and co-founder and Managing Editor of International Journal of Computers Communications & Control and co-founder of International Conference Computers Communications & Control**.**
His publications and research results consist in 10 books, over 60 papers and 2 inventions.

Program Committee

- Răzvan ANDONIE, *Central Washington University, USA* (Senior Member of IEEE)
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- Ramona URZICEANU, Agora University of Oradea, Romania.

Keynote Speakers at ICCCC 2018

May 8-12, Oradea, Romania

1. Dr. Barnabas BEDE

Tutorial: *Advances in Fuzzy Systems*

Prof. Barnabas Bede, Department Chair, Mathematics, DigiPen Institute of Technology, Redmond, Washington, USA, E-mail: bbede@digipen.edu

Dr. Barnabas Bede earned his Ph.D. in Mathematics from Babes-Bolyai University of Cluj-Napoca, Romania. His research interests include Fuzzy Sets, Fuzzy Logic and Image Processing. He serves as editorial board member of the journals *Fuzzy Sets and Systems* and *Information Sciences*.

He joined DigiPen in 2011. Before that, he held positions in Romania, Hungary, University of Texas at El Paso and University of Texas-Pan American.

At DigiPen he develops class material for Fuzzy Sets and various topics in Mathematical Analysis.

Selected publications:

- B. Bede, S.G. Gal, "Generalizations of the Differentiability of Fuzzy-Number-Valued Functions with Applications to Fuzzy Differential Equations," *Fuzzy Sets and Systems* 151(2005) 581-599.
- B. Bede, "Mathematics of Fuzzy Sets and Fuzzy Logic," *Springer*, 2013

2. Dr. Valeriu BEIU

Tutorial: Brain-inspired Nano-architectures

Prof. Valeriu Beiu, Department of Mathematics and Computer Science, Aurel Vlaicu University of Arad, Romania, E-mail: valeriu.beiu@uav.ro

Valeriu Beiu (S'92–M'95–SM'96) received the MSc in CE from the University "Politehnica" Bucharest (UPB) in 1980, and the PhD summa cum laude in EE from the Katholieke Universiteit Leuven (KUL) in 1994.

His affiliations include the Research Institute for Computer Techniques, UPB, KUL, King's College London, Los Alamos National Laboratory, Rose Research, Washington State University, United Arab Emirates University, and "Aurel Vlaicu" University of Arad, while his research interests have constantly been on biological-inspired nano-circuits and brain-inspired nano-architectures (low-power, highly reliable, massively parallel), being funded at over US$ 41M, and publishing over 250 papers (42 invited and 11 patents) as well as giving over 190 invited talks and organizing over 100 conferences.

Dr. Beiu received 5 fellowships and 7 best paper awards, and is a member of ACM, INNS, ENNS, and MCFA. He was a member of the SRC-NNI Working Group on Novel Nano-architectures, the IEEE CS Task Force on Nano-architectures, and the IEEE Emerging Technologies Group on Nanoscale Communications, and has been an Associate Editor of the *IEEE Transactions on Neural Networks*, *Nano Communication Networks*, and *IEEE Transactions for Very Large Scale Integration Systems*.

Selected publications:

- V. Beiu, J.M. Quintana, M.J. Avedillo, VLSI implementations of threshold logic-a comprehensive survey, IEEE Transactions on Neural Networks 14 (5), 1217-1243.
- S. Roy, V. Beiu, Majority multiplexing-economical redundant fault-tolerant designs for nanoarchitectures, IEEE Transactions on Nanotechnology 4 (4), 441-451.

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3. Dr. Yezid DONOSO

Tutorial: *Methodologies for Solving Complex Multi-Objective Combinatorial Problems in Engineering: An Evolutionary Approach Applied to Computer Networks*

Prof. Yezid DONOSO, *Head of the Information Security Postgraduate and Master Program, Systems and Computing Engineering Department, Universidad de los Andes, Cra. 1 Este No. 19A-40, Phone 57-1-3394949 Ext 1723, Bogotá, Colombia, South America,* E-mail: ydonoso@uniandes.edu.co

Yezid Donoso received the System and Computing Engineering degree from Universidad del Norte, Barranquilla, Colombia, in 1996 (IEEE M'02 – SM'05) , M.S. degree from Universidad de los Andes, Bogotá, Colombia, in 1998, D.E.A. in Information Technology from Girona University, Girona, Spain, in 2002 and the Ph.D. (Cum Laude) in Information Technology from Girona University, Spain, in 2005. He is currently Associate Professor at Universidad de los Andes, Colombia and Coordinator of the Information Security Master Program and Academic ViceDirector of the Department. He is Senior Member IEEE, DVP (Distinguished Visitor Professor) Computer Society IEEE from 2005 to 2009 and President IEEE Colombia Section 2013 – 2014. Dr. Donoso has several national and international awards and medals. Biography published in the books: "Who's Who in the World", edition 2006 and "Who's Who in Science and Engineering" by Marquis Who's Who in the World and "2000 Outstanding Intellectuals of the 21th Century" by International Biographical Centre. Cambridge, England, 2006. "Distinguished Professor", given by Universidad del Norte. Colombia. Octubre 2004. National Award of Operations research given by The Colombian Society of Operations Research. 2004..He is the co-author of several books, including Multi-Objective Optimization in Computer Networks Using Metaheuristics and Network Design for IP Convergence and He has more than 240 papers published between journals and conferences.

Selected publications:

- Marentes L, Wolf T, Nagurney A, Donoso Y. (2016) Towards Pricing Mechanisms for Delay Tolerant Services.International Journal of Computers, Communications and Control (ISSN 1841-9836) 11 (1), pp. 77-89.
- Velasquez C, Donoso Y. (2017) Delay/Disruption Tolerant Networking-Based Routing for Rural Internet Connectivity (DRINC). International Journal of Computers, Communications and Control (ISSN 1841-9836) 12 (1), pp. 131-147.

Selected books:

- Donoso Y, Fabregat R. (2007) Multi-Objective Optimization in Computer Networks Using Metaheuristics. Auerbach Publications. (ISBN 0849380847) USA.
- Donoso Y. (2009) Network Design for IP Convergence. CRC Press. (ISBN 978-1420067507) USA.

4. Dr. Arturas KAKLAUSKAS

Tutorial: *Body Language and Affective Computing*

Prof. Arturas Kaklauskas, Chairman of the Department of Construction Economics and Property Management, Director of the Research Institute of Smart Building Technologies, Member of the Lithuanian Academy of Science, Vilnius Gediminas Technical University, Lithuania, E-mail: arturas.kaklauskas@vgtu.lt

Web of Science Citation Report for Prof. A. Kaklauskas:

- Published papers: 141; H-Index: 23; Sum of times cited: 1764; Average citation per paper: 12.39

PhD Dr.Sc. A. Kaklauskas is a professor at Vilnius Gediminas Technical University, in Lithuania; Director of the Research Institute of Smart Building Technologies; Head of the Department of Construction Economics and Property Management; laureate of the Lithuanian Science Prize; member of the Lithuanian Academy of Sciences and editor of Engineering Applications of Artificial Intelligence, an international journal. He contributed to nine Framework programme, two HORIZON2020 projects and participated in over 30 other projects in the EU, US, Africa and Asia. The Belarusian State Technological University (Minsk, Belarus) awarded him an Honorary Doctorate in 2014. His publications include nine books and 121 papers in Web of Science Journals. Fifteen PhD students successfully defended their theses under his supervision. The Web of Science H-Index of Prof. A. Kaklauskas is 23. Web of Science Journals have cited him 1764 times. His areas of interest include multiple criteria decision analysis, intelligent decision support systems, affective computing, big data analytics, intelligent tutoring systems, intelligent library, internet of things, life cycle analyses of built environments, energy, climate change, resilience management, healthy houses, sustainable built environments, etc.

Selected publications:

- Body Language and Affective Computing
- Environmental sustainability development in post-Soviet states over 25 years: An integrated multiple criteria analysis

Special Sessions of ICCCC2018

Special Session 1: *Network Optimization and Security*

Organizers and Chairs:
Prof. Yezid DONOSO, Columbia
Prof. Dominic BUCERZAN, Romania

Session Scope:

The convergence in communication networks and computing has led the exponential growth of new applications and information systems. Nowadays, users and applications generate and request more data demanding efficient and secure management. New algorithms are needed to manage the network resource allocation improving the network performance, response against failures, congestion and attacks; and to avoid loss of confidentiality, integrity or availability in the network.

Session Topics

T.1.1. Network Optimization:

Advanced Network Architecture; Computational complexity and data structures; Distributed Algorithms for control and management in Communication Systems; Energy Efficiency in Wireless Networks; Mobility, Handoff, and Location Management; Network Algorithm analysis; Network Structure, Routing and Resource Management; Networks Survivability against Failures, Congestion and Attacks; Network Planning; Quality of Service / Quality of Experience Optimization; Software Define Network; Scheduling and Network Optimization; Self-Organizing Networks; Reliable Networks; Special Topics in Network Optimization.

T.1.2. Security:

Intrusion; Detection and Prevention Systems; Network Authentication and Key Management; Network Reliability; Privacy and Anonymity; Secure Networking; Secure Network Protocols; Security for Cloud Networking; Security for Internet Applications; Security for Wireless Sensor networks; Security for Smart Grids; Security for Vehicular Networks; Security for Critical Infrastructures; Special Topics in Security.

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Special Session 2: *Advances in Fuzzy Systems*

Organizer and Chair: Prof. Barnabas BEDE, USA

Session scope:

Fuzzy (Inference) Systems constitute the main applications of Fuzzy Sets and Fuzzy Logic. The two main types of fuzzy systems were introduced by Mamdani and Assilian (MA) in 1975, and Takagi, Sugeno and Kang (TSK) in 1985 and 1988. Subsequently, in 1993 Yang introduced a fuzzy system of TSK type called Adaptive Network-based Fuzzy Inference System (ANFIS), and developed an efficient learning algorithm for them. Since then, these three architectures were widely used in applications in various areas of Science and Engineering, however, their theoretical properties received less attention. The present special session proposes to ignite new ideas leading to both theoretical and practical advances in Fuzzy Systems.

Topic 2.1:

Theoretical aspects that the session is interested in, are: approximation properties, higher order fuzzy systems, novel types and architectures of fuzzy systems and their connection to Neural Networks and other Machine Learning techniques. Interpretability of fuzzy systems, motivates their use in knowledge extraction and knowledge representation.

Topic 2.2.:

Application areas ranging from control systems to various novel research areas are intended to be discussed as well. The main areas sought to be infused with Fuzzy Sets, Fuzzy Logic and Neural Networks, are overlapping with Machine Learning techniques. Other interesting questions to be answered are comparison of various types of fuzzy systems.

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Special Session 3: *Brain-inspired Nano-architectures*

Organizer and Chair: Prof. Valeriu BEIU, Romania

Session scope

Brain-inspired systems are, most often than not, equated to artificial neural networks. When adding nano-architectures it is to be expected that most if not all would think of neuromorphic chips. In fact, here our view is different. The *raison d'être* is the maturation over the last half-century of several fields into *A Nanotechnology-Inspired Grand Challenge for Future Computing* aiming to develop *"a new type of computer that can proactively interpret and learn from data, solve unfamiliar problems using what it has learned, and operate with the energy efficiency of the human brain."* It is fascinating now to ponder on what Robert Noyce, co-founder of Intel, stated in 1984: *"Until now we have been going the other way; that is, in order to understand the brain we have used the computer as a model of it. Perhaps it is time to reverse this reasoning: to understand where we should go with the computer, we should look to the brain for some clues"* (The Next 100 Years, IEEE Centenary). That is why this special session aims to be a melting pot of ideas looking at the brain (brain-inspired) from different angles, trying to foresee how future computing based on nano-devices (nano-architectures) might look like.

Topic 3.1.

Anything reliability-related trying to find answers to questions like:

3.1.1. What is the meaning of computing when the elementary nano-devices no longer exhibit the level of determinism required by the classical von Neumann architecture?

3.1.2. Which are the alternate computing models that acknowledge nano-device variations and errors, e.g., by embracing their intrinsically statistical behaviors?

3.1.3. Which are the new design principles and system theory based on such variation - aware models?

3.1.4. What are the fundamental limits of computing when entering this realm where reliable-enough computing is achieved with imperfect (crummy) components?

Topic 3.2.

Any directions of research with the potential to close the power/energy gap (between brain and chips), trying to answer questions like:

3.2.1. Which are (some of) the fundamental reasons for the gap?
3.2.2. Which would be the advantages of changing the information carrier, or of using more than one information carrier?
3.2.3. Which type of communication ("wire") should we use, and should we use more than one type? This is because Carver Mead stated that "we lose a factor of about 100 because [...] we spend most of our energy charging up the wires and not the gate"
3.2.4. Which are the roles crowding plays in this quest, and how to take advantage of these?

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Special Session 4: *Affective Computing*

Organizer and Chair: Acad. Arturas KAKLAUSKAS, Lituania

Session Scope:

Affective computing is presently one of the most dynamic scientific matters, having progressively exhaustive consideration. This robust attention is determined by a varied range of up-to-date systems and devices in many fields, such as smart systems, e-learning, market research, robotics, social monitoring, advertisement, medicine, security, smart cities, smart buildings, smart cars, virtual reality, affective agents, collaboration apps, recommender systems, etc. Affective computing concerns multidisciplinary background knowledge, such as psychology, cognitive and computer sciences.

Session Topics

T.4.1. Technologies:

Emotional speech (algorithms, databases, speech descriptors), facial affect detection (facial expression databases, emotion classification, facial action coding system, challenges in facial detection), behavioral (the pattern of behavior of a person, including but not limited to typing rhythm, gait, and voice), body gesture, physiological monitoring (fingerprint, palm veins, DNA, palm print, hand geometry, iris recognition, retina and odour/scent, blood volume pulse, galvanic skin response), visual aesthetics.

T.4.2. Applications:

Smart systems, e-learning, market research, robotics, social monitoring, advertisement, medicine, security, smart cities, smart buildings, smart cars, virtual reality, affective agents, collaboration apps, recommender systems, financial, consumer/residential systems, healthcare, logical access control, military, construction, chemistry, automotive industry, electronic industry, power engineering, hospitality industry, food industry, software industry, entertainment industry, cultural industry.

Deadlines ICCCC2018

- Abstract or Full Paper Draft Submission: *November 15, 2017*
- Notification of Acceptance/Rejection of Abstracts: *December 1, 2017*
- Full Paper Submission : *January 15, 2018*
- Notification of Acceptance/Rejection of Papers: *February 15, 2018.*
- Final Submission of Accepted Papers: *March 15, 2018.*
- Conference Fees Transfer**:** *February 16- March 15, 2018*

Note*:* All submissions will be performed via *EasyChair:*
https://easychair.org/my/conference.cgi?a=12804438;conf=icccc2018

ICCCC2018 Fees

Conference Fees (online system via credit card payment or via bank transfer will be open between February 16- March 15, 2018*):*

Variant 1. Basic variant: conference fee is 600 € (without accommodation)
that includes:

- full access to the conference program and proceedings paper publications;
- conference materials and coffee breaks;
- reception banquets and daily meals (lunch and dinner on May 09-11);
- excursion (May 11, Mountain Resort, meals included).

Variant 2. Full variant: conference fee is 800 €

- accommodation at four stars Hotel in Baile Felix on May 08/09, 09/10, 10/11, 11/12: President Hotel****:

http://www.baile-felix.ro/en/

- full access to the conference program and proceedings paper publications;
- conference materials and coffee breaks;
- reception banquets and daily meals (lunch and dinner on May 09-11);
- excursion (May 11, Mountain Resort, meals included).
- free access to the Hotel President facilities: indoor pool / outdoor pool with thermal water; sauna and Jacuzzi; fitness; sport facilities (football, tennis); guarded indoor parking; Internet - Wireless.

Discounts (please send a request to icccc@univagora.ro)

- There is no discount for the participants that cannot participate to the full program of the conference.
- We can accept request for discount (10%) from PhD students.
- Discount for a co-author, an attendant without paper or an accompanying person (family member) is 50%.

ICCCC2018 Venue Information & Contact

USEFUL LINKS

- About Oradea:http://oradea.travel/en/
- The Romanian Visa
- Hotel President****: http://www.baile-felix.ro/en/
- Oradea Airport (We will wait in airport at Arrival Gate)
- Oradea Train Station (We will wait at Information Office)
- Oradea Bus Station

If you are planning to arrive by airplane:
The closest cities with airports are:

1. ***Oradea***, flying with Tarom, Blue Air, Raynair: Oradea International Airport
2. ***Debrecen*** *(Hungary)* – 1 h- 1h30 to Oradea by bus – flights from Eindhoven, Dortmund, London with Wizzair: http://wizzair.com/en-GB/Search

3. ***Cluj Napoca***, in Romania (2h30-3h to Oradea by bus) – flights from Bucharest, Vienna, Eindhoven, Dortmund, Frankfurt, Munich, Brussels, Paris, Rome, Barcelona, Madrid, Tel-Aviv, London, with Wizzair: http://wizzair.com/en-GB/Search
4. ***Timisoara***, in Romania (2h-2h30 to Oradea by bus) – flights from Bucharest, Paris, London, Dortmund, Munich, Bologna, Milan, Rome, Barcelona, Madrid, with Wizzair: http://wizzair.com/en-GB/Search
5. ***Budapest*** *(Hungary)* – 3h30-4h to Oradea by bus, or 5 hours by train – you choose your flight to Budapest International Airport (http://www.bud.hu/english)

Shuttle Buses from the Airports to Oradea:

You can book your airport transfer also, separately, via the following websites:

- http://www.eurolines.ro/article--When-and-with-what--1971.html
- http://www.transilvaniatravel.ro/transfer-aeroport/
- http://www.rentacarairport.ro/home-en
- http://www.transportautocar.eu/timetable.html
- http://www.airantares.ro/transport-microbuze-ro6.html

Contact

Agora University, R&D Agora
Piata Tineretului, 8
Tel./Fax: +40 359 101 032
E-mail: icccc@univagora.ro, rector@univagora.ro,
professor.ioan.dzitac@ieee.org